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cont* wherein the processing circuitry comprises a setup engine that determines a bounding box for pixels manipulated by the instruction, wherein the bounding box contains all edges of a macroblock.

D2 43. (Amended) The method of claim 41, further comprising determining a bounding box for pixels manipulated by the command, wherein the bounding box contains all edges of a macroblock.

46. (Amended) The method of claim 45, wherein performing frame prediction operations further comprises:

D3 generating a bounding box containing the macroblock; and
iterating the bounding box;
fetching reference pixels;
filtering the reference pixels;
averaging the filtered reference pixels, if necessary; and
adding correction data to the reference pixels.

49. (Amended) The article of manufacture of claim 48, wherein the electronically accessible medium further comprises instructions that, when executed by one or more processors, cause the one or more processors to

*D4
cont* generate a bounding box containing the macroblock; and
iterate the bounding box;
fetch reference pixels;

filter the reference pixels;

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cont.*

average the filtered reference pixels, if necessary; and

add correction data to the reference pixels.
